

HE-

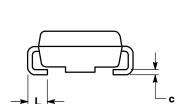
SMA CASE 403D-02 ISSUE D

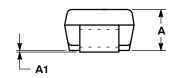
DATE 24 OCT 2007



- DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. 403D-01 OBSOLETE, NEW STANDARD IS 403D-02.

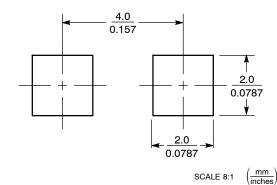
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.92	2.17	2.27	0.076	0.085	0.089
A1	0.05	0.10	0.15	0.002	0.004	0.006
b	1.27	1.45	1.63	0.050	0.057	0.064
С	0.15	0.28	0.41	0.006	0.011	0.016
D	2.29	2.60	2.92	0.090	0.103	0.115
E	4.06	4.32	4.57	0.160	0.170	0.180
HE	4.83	5.21	5.59	0.190	0.205	0.220
L	0.76	1.14	1.52	0.030	0.045	0.060





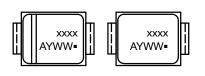
SOLDERING FOOTPRINT*

POLARITY INDICATOR OPTIONAL AS NEEDED (SEE STYLES)



^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



STYLE 1 STYLE 2

= Specific Device Code XXXX = Assembly Location Α Υ = Year

WW = Work Week = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1: PIN 1. CATHODE (POLARITY BAND) STYLE 2:

NO POLARITY

DOCUMENT NUMBER:	98AON04079D	E
STATUS:	ON SEMICONDUCTOR STANDARD	a v
NEW STANDARD:		"(
DESCRIPTION:	SMA	

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98AON04079	D

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ISSUE REVISION DATE						
ISSUE	ISSUE REVISION					
В	ADDED POLARITY NOTE AND STYLES. REQ. BY D. CULBERTSON.	10 FEB 2005				
С	ADDED NOMINAL VALUES AND UPDATED MARKING DIAGRAM. REQ. BY HONG XIAO.	03 AUG 2005				
D	CORRECTED A DIMENSIONS TO 1.92, 2.17, 2.27 MM & 0.076, 0.085, 0.089 INCH. REQ. BY D. TRUHITTE.	24 OCT 2007				

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