MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

ON Semiconductor

DATE 07 JUL 2008



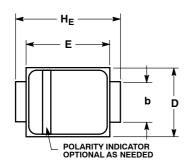


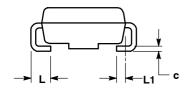
SMB CASE 403A-03 ISSUE G

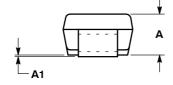
SCALE 1:1

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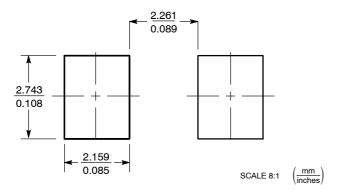
Polarity Band Non-Polarity Band







SOLDERING FOOTPRINT*



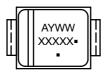
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

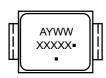
NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
- 3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.90	2.13	2.45	0.075	0.084	0.096
A1	0.05	0.10	0.20	0.002	0.004	0.008
b	1.96	2.03	2.20	0.077	0.080	0.087
С	0.15	0.23	0.31	0.006	0.009	0.012
D	3.30	3.56	3.95	0.130	0.140	0.156
E	4.06	4.32	4.60	0.160	0.170	0.181
HE	5.21	5.44	5.60	0.205	0.214	0.220
L	0.76	1.02	1.60	0.030	0.040	0.063
L1	0.51 REF			0.020 REF		

GENERIC MARKING DIAGRAM*





Polarity Band

Non-Polarity Band

XXXXX = Specific Device Code Α = Assembly Location

Υ = Year WW = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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ISSUE	REVISION	DATE
E	ADDED NOMINAL VALUES, MARKING DIAGRAM AND SOLDERING FOOTPRINT. REQ. BY HONG XIAO.	03 AUG 2005
F	UPDATED MAXIMUM DIMENSION LIMITS TO JEDEC STANDARDS. REQ. BY LOUIS TSAI.	30 NOV 2005
G	ADDED NON-POLARITY BAND OPTION. CORRECTED DEVICE MARKING. REQ. BY D. TRUHITTE.	07 JUL 2008

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